

# FPGA Processor Board

## LVDS Converter for

**NLT NL192108AC10-01D  
KOE TX18D200VM0EAA10  
KOE TX18D204VM0BAA**

### Specification



- ◆ This LVDS converter receives dual-link LVDS, then an integrated FPGA combines into single link LVDS at doubled clock rate.
  - NLT 9", NL192108AC10-01D, Full HD model
  - KOE (JDI) 7", TX18D200VM0EAA10, Full HD model
- ◆ This converter converts JEIDA format to VESA format, so thus, all 8bit and 10bits format can be fitted into 10 bit panel. However, this converter does NOT enhance quality of 8 bits source.
  - All kinds of LVDS based 10 bit Displays from 8 bit source (JEIDA-to-VESA conversion)
- ◆ This converter board takes a role of intermediate device in between ordinary AD board, SBC (single board computer) or Embedded PC and TFT LCD Display (NLT 9" or KOE(JDI) 7").

#### ◆ Basic Features

- LVDS Input
- LVDS Output
- LVDS Line Buffer
- LVDS Pixel Formatter
- Vcc in:
  - Min 4.8V
  - Typ 5.0V
  - Max 5.2V

#### ◆ Conditions

- Operating Temperature : -10°C~70°C
- Operating Humidity : 20%~90%RH
- Storage Temperature : -20°C~70°C
- Storage Humidity : 5%~90%RH

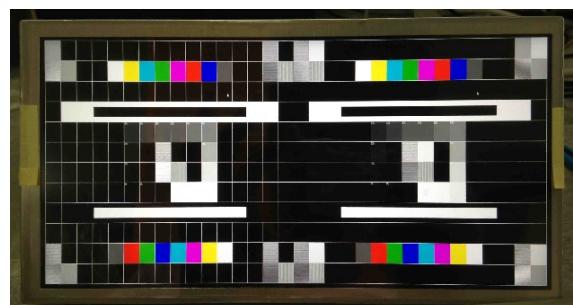
#### ◆ Dimensions

- 55mm X 80mm X 7.3mm



**NL192108AC10-01D**

(960 x 1080) + (960 x 1080) resolution



**NL192108AC10-01D + LVDS Converter**

(1920 x 1080) resolution